### Switch Inputs

Switch 1: From SW1\_POS PTD7 (J2 pin 19) to ground (J2 pin 14). Uses internal pull-up resistor.

Switch 2: From SW2\_POS PTD6 (J2 pin 17) to ground (J2 pin 14). Uses internal pull-up resistor.

### Debug Signals

For logic analyzer – supported in code versions >= V2

|  |  |  |
| --- | --- | --- |
| **Task** | **Debug Signal** | **Port & Bit** |
| Task\_ReadSwitches | DBG\_1 | PTB1 |
| Task\_Flash | DBG\_2 | PTB2 |
| Task\_RGB | DBG\_3 | PTB3 |